

[ABSTRACT OF THE DISCLOSURE]

The present invention provides a debug system in which both a debug operation for a microprocessor and security of information stored in the debug system are achieved.

5 According to the microprocessor of the present invention, when a user inputs a key code on a host PC, the microprocessor stored the key code therein and also encrypts an instruction and data using the key code so as to send it to the host PC. Even when a vicious user connects a microprocessor to a debug
10 unit and obtains an encrypted instruction and encrypted data, s/he can not decrypt the encrypted instruction and the encrypted data unless s/he knows the key code.